



# POTTI SRIRAMULU CHALAVADI MALLIKHARJUNA RAO COLLEGE OF ENGINEERING & TECHNOLOGY

Sponsored by: SKPVV Hindu High School Committee, Estd:  
1906 (Approved by AICTE Affiliated to JNTU Kakinada)

ISO 9001:2015 Certified

Raghavareddy Street, Kothapeta, Vijayawada-520001.

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## FACULTY PROFILE

T.Durga Prasad  
S/o : T. Krishna Murthy  
Door No :4-1-155,  
Karanamgari street,  
Bhimanavaripet,  
Near Milk Project,  
Vijayawada-520001  
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### **Experience:**

Teaching: 2 Years  
Industry: Nil

### **Career Profile**

Year	Organization	Role
2016 to till date	Potti Sriramulu Chalavadi Mallikharjuna Rao college of Engg. And Tech., Vijayawada.	Assistant Professor

### **Qualification**

Degree and Date	Board / University	Date of Completion	% Marks
M.Tech	JNTUK	September 2016	75.4 (CGPA)
B.Tech	JNTUK	July 2014	71.78
Intermediate	Board of Intermediate Education	April 2010	87
SSC	Board of secondary education	April 2008	83

**The subjects handled:**

1. Switching theory and logic design
2. Low Power IC Design
3. Computer Networks

**Certifications**

Certification	Date Certified
NIL	

**Affiliation to Professional Bodies**

1. I-Explore International Research Journal Consortium (IIRJC).
2. International association of engineers IAENG.

**Publications**

1. “ENERGY EFFICIENT CODE CONVERTERS USING MODIFIED POSITIVE FEEDBACK ADIABATIC LOGIC”, published in International Journal of Innovative Research in Engineering and Applied Sciences, August 2017.
2. “PATIENT MONITORING SYSTEM USING GSM”, published in International Journal of Science and Innovative Engineering & Technology, May 2017.

**Conferences, Seminars, Workshops & Guest Lectures Attended**

1. Two day National level workshop on “Advances in Electronics and Communication Engineering” from 26<sup>th</sup> – 27<sup>th</sup> August 2017 at JNTUK, Kakinada.

**Events organized/ Conducted**

Nil

**Funding Projects Handled (If any – Please provide the details of the project)**

Nil

**Signature**

T. Durga Prasad.